



- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

## Product Summary



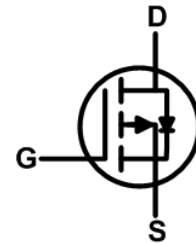
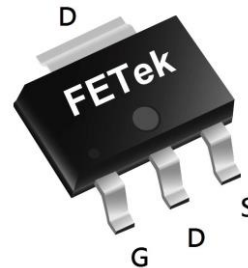
| BVDSS | RDSON | ID    |
|-------|-------|-------|
| -30V  | 52mΩ  | -4.5A |

## Description

The FKL3101 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The FKL3101 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

## SOT223 Pin Configuration



## Absolute Maximum Ratings

| Symbol                     | Parameter  | Rating     | Units            |
|----------------------------|--|------------|------------------|
| $V_{DS}$                   | Drain-Source Voltage                               | -30        | V                |
| $V_{GS}$                   | Gate-Source Voltage                                | $\pm 20$   | V                |
| $I_D@T_A=25^\circ\text{C}$ | Continuous Drain Current, $V_{GS} @ -10\text{V}^1$ | -4.5       | A                |
| $I_D@T_A=70^\circ\text{C}$ | Continuous Drain Current, $V_{GS} @ -10\text{V}^1$ | -3.5       | A                |
| $I_{DM}$                   | Pulsed Drain Current <sup>2</sup>                  | -23        | A                |
| $P_D@T_A=25^\circ\text{C}$ | Total Power Dissipation <sup>3</sup>               | 1.5        | W                |
| $T_{STG}$                  | Storage Temperature Range                          | -55 to 150 | $^\circ\text{C}$ |
| $T_J$                      | Operating Junction Temperature Range               | -55 to 150 | $^\circ\text{C}$ |

## Thermal Data

| Symbol          | Parameter  | Typ. | Max. | Unit               |
|-----------------|--|------|------|--------------------|
| $R_{\theta JA}$ | Thermal Resistance Junction-Ambient <sup>1</sup> | ---  | 85   | $^\circ\text{C/W}$ |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case <sup>1</sup>    | ---  | 48   | $^\circ\text{C/W}$ |



**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

| Symbol                              | Parameter                                      | Conditions   | Min. | Typ.   | Max. | Unit  |
|-------------------------------------|--|--|------|--------|------|-------|
| BV <sub>DSS</sub>                   | Drain-Source Breakdown Voltage                 | V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA  | -30  | ---    | ---  | V     |
| ΔBV <sub>DSS</sub> /ΔT <sub>J</sub> | BV <sub>DSS</sub> Temperature Coefficient      | Reference to 25°C, I <sub>D</sub> =-1mA  | ---  | -0.023 | ---  | V/°C  |
| R <sub>DS(ON)</sub>                 | Static Drain-Source On-Resistance <sup>2</sup> | V <sub>GS</sub> =-10V, I <sub>D</sub> =-4A   | ---  | 42     | 52   | mΩ    |
|                                     |  | V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3A  | ---  | 75     | 90   |       |
| V <sub>GS(th)</sub>                 | Gate Threshold Voltage                         | V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA                                  | -1.2 | -1.6   | -2.5 | V     |
| ΔV <sub>GS(th)</sub>                | V <sub>GS(th)</sub> Temperature Coefficient    |  | ---  | 4      | ---  | mV/°C |
| I <sub>DSS</sub>                    | Drain-Source Leakage Current                   | V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C                           | ---  | ---    | -1   | uA    |
|                                     |  | V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C                           | ---  | ---    | -5   |       |
| I <sub>GSS</sub>                    | Gate-Source Leakage Current                    | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V   | ---  | ---    | ±100 | nA    |
| g <sub>fs</sub>                     | Forward Transconductance                       | V <sub>DS</sub> =-5V, I <sub>D</sub> =-4A  | ---  | 11     | ---  | S     |
| Q <sub>g</sub>                      | Total Gate Charge (-4.5V)                      | V <sub>DS</sub> =-15V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A                         | ---  | 6.4    | 9.0  | nC    |
| Q <sub>gs</sub>                     | Gate-Source Charge                             |  | ---  | 2.3    | 3.2  |       |
| Q <sub>gd</sub>                     | Gate-Drain Charge                              |  | ---  | 1.9    | 2.7  |       |
| T <sub>d(on)</sub>                  | Turn-On Delay Time                             | V <sub>DD</sub> =-15V, V <sub>GS</sub> =-10V, R <sub>G</sub> =3.3Ω,<br>I <sub>D</sub> =-4A | ---  | 2.8    | 5.6  | ns    |
| T <sub>r</sub>                      | Rise Time                                      |  | ---  | 8.4    | 15.1 |       |
| T <sub>d(off)</sub>                 | Turn-Off Delay Time                            |  | ---  | 39     | 78.0 |       |
| T <sub>f</sub>                      | Fall Time                                      |  | ---  | 6      | 12.0 |       |
| C <sub>iss</sub>                    | Input Capacitance                              | V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz   | ---  | 583    | 816  | pF    |
| C <sub>oss</sub>                    | Output Capacitance                             |  | ---  | 100    | 140  |       |
| C <sub>rss</sub>                    | Reverse Transfer Capacitance                   |  | ---  | 80     | 112  |       |

**Diode Characteristics**

| Symbol          | Parameter                                | Conditions   | Min. | Typ. | Max. | Unit |
|-----------------|--|--|------|------|------|------|
| I <sub>S</sub>  | Continuous Source Current <sup>1,4</sup> | V <sub>G</sub> =V <sub>D</sub> =0V, Force Current              | ---  | ---  | -4.5 | A    |
| I <sub>SM</sub> | Pulsed Source Current <sup>2,4</sup>     |  | ---  | ---  | -23  | A    |
| V <sub>SD</sub> | Diode Forward Voltage <sup>2</sup>       | V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25°C | ---  | ---  | -1.2 | V    |
| t <sub>rr</sub> | Reverse Recovery Time                    | I <sub>F</sub> =-4A, dI/dt=100A/μs, T <sub>J</sub> =25°C       | ---  | 7.8  | ---  | nS   |
| Q <sub>rr</sub> | Reverse Recovery Charge                  |  | ---  | 2.5  | ---  | nC   |

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.

Typical Characteristics

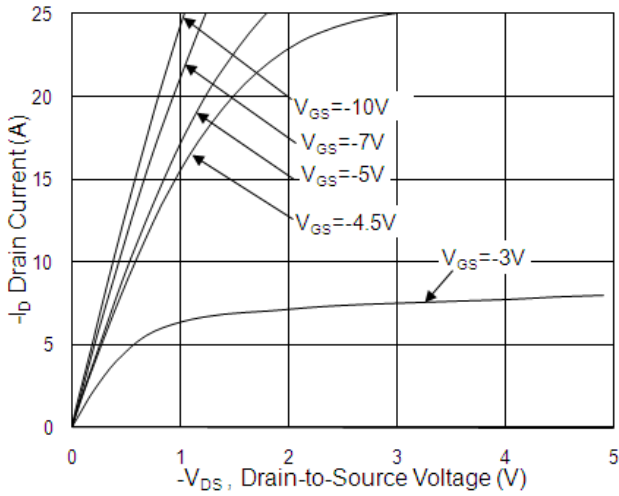


Fig.1 Typical Output Characteristics

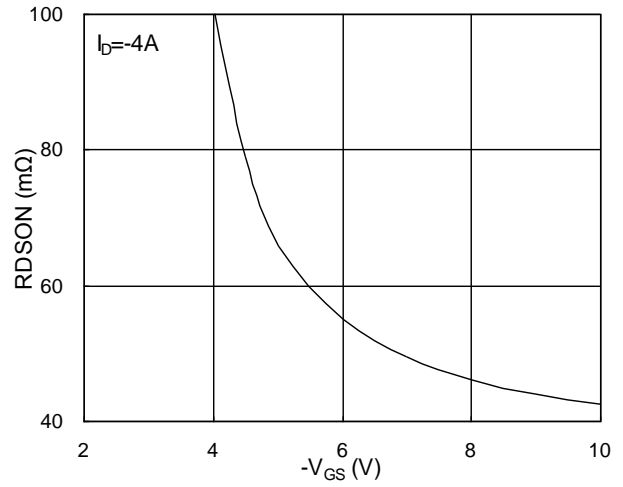


Fig.2 On-Resistance v.s Gate-Source

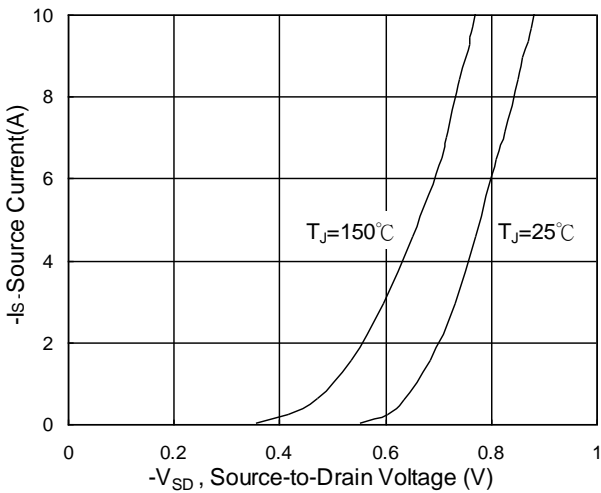


Fig.3 Forward Characteristics Of Reverse

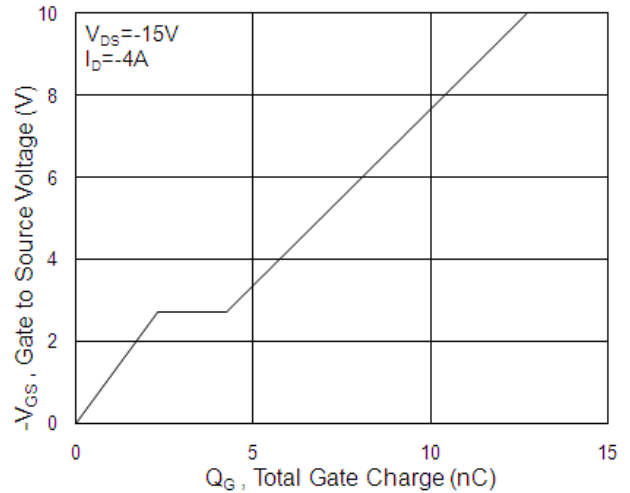


Fig.4 Gate Charge Characteristics

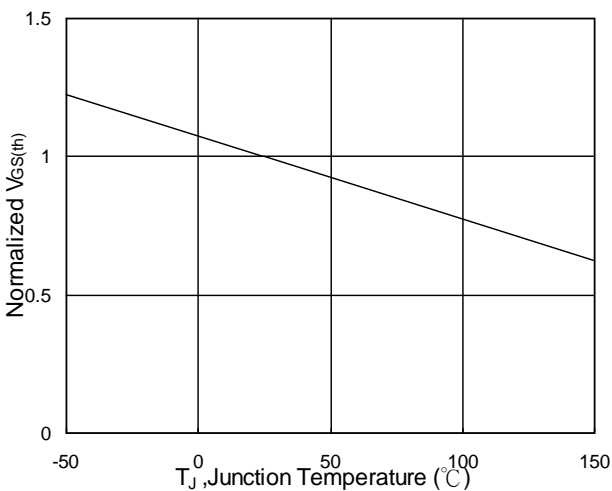


Fig.5 Normalized  $V_{GS(th)}$  v.s  $T_J$

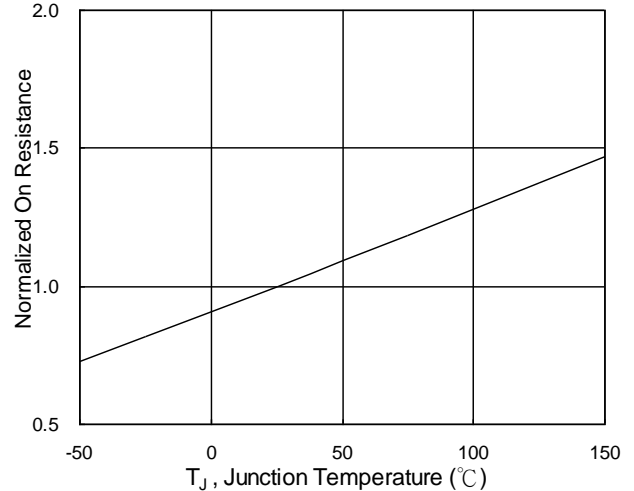


Fig.6 Normalized  $R_{DS(on)}$  v.s  $T_J$

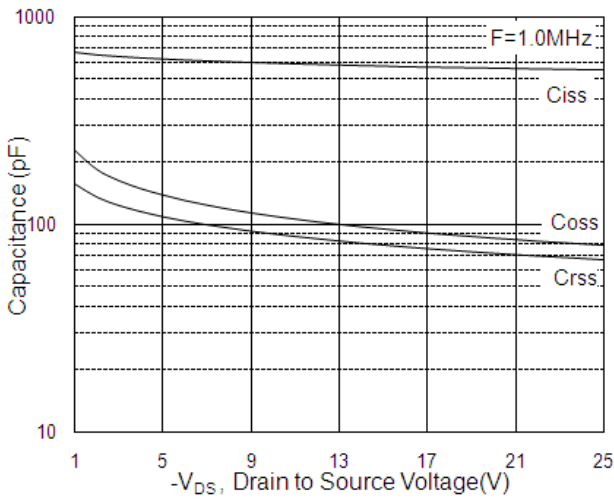


Fig.7 Capacitance

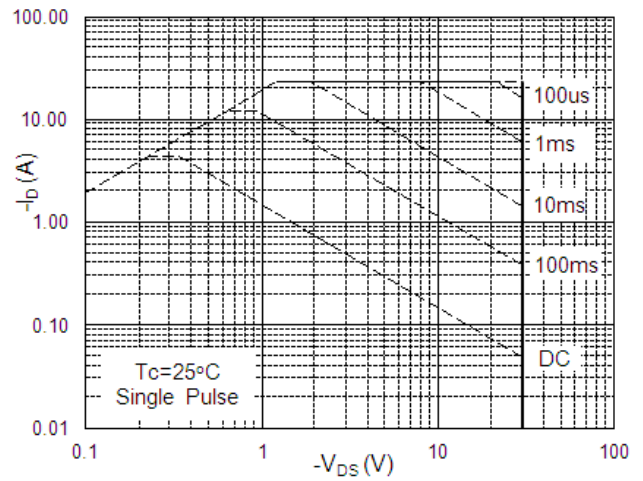


Fig.8 Safe Operating Area

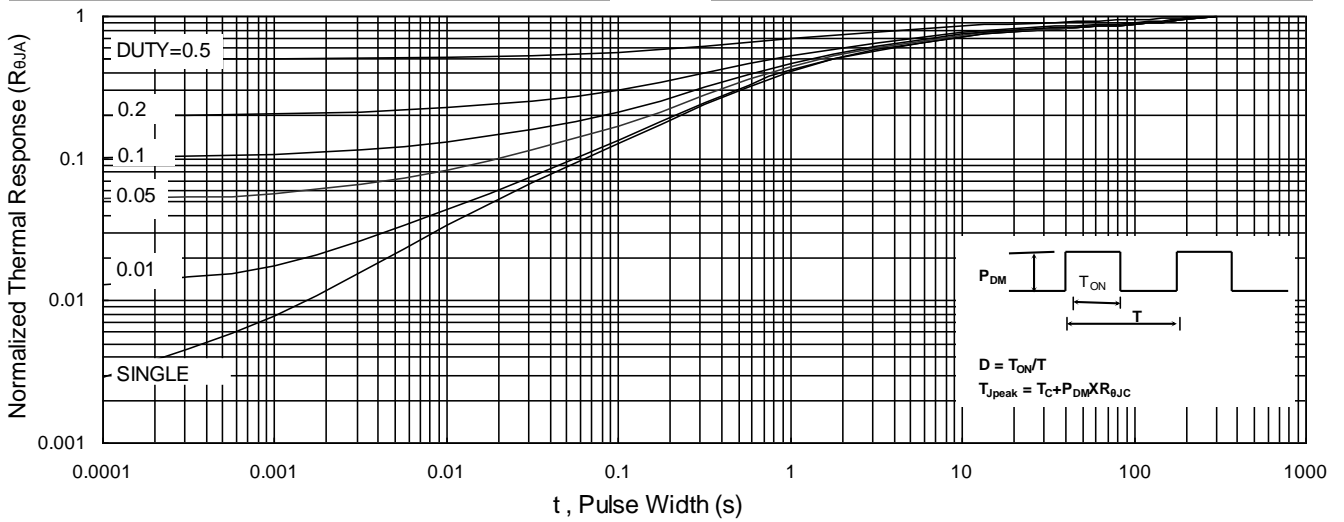


Fig.9 Normalized Maximum Transient Thermal Impedance

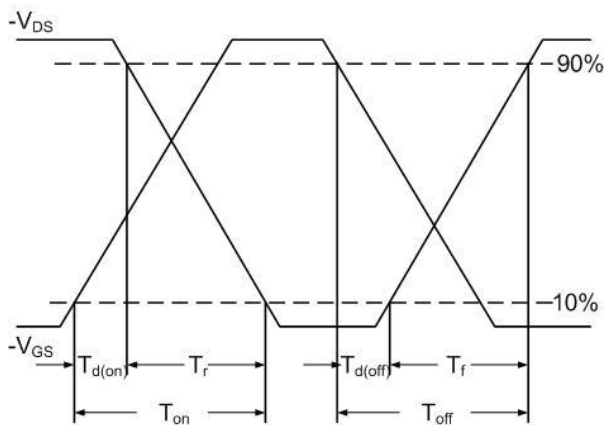


Fig.10 Switching Time Waveform

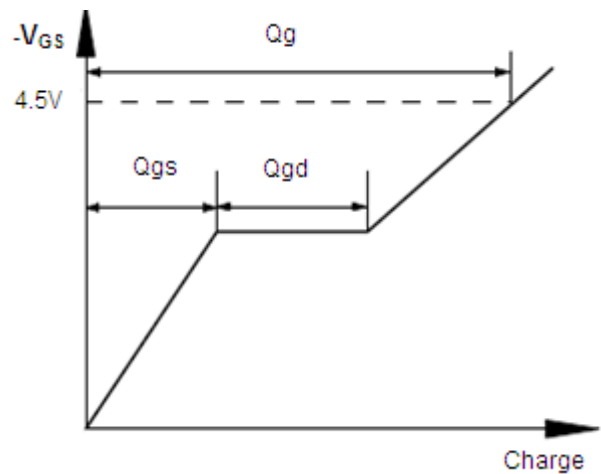


Fig.11 Gate Charge Waveform