

**Features**

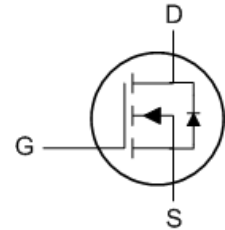
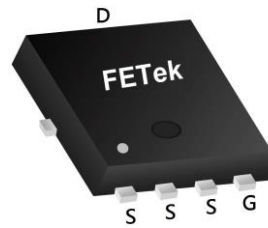
- ★ Advanced Trench MOS Technology
- ★ 100% EAS Guaranteed
- ★ Green Device Available

**Applications**

- ★ Synchronous rectification.
- ★ Primary side switching.

**Product Summary**


BVDSS	RDSON	ID
80V	6.5mΩ	60A

**PRPAK5X6 Pin Configuration**

**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	80	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_C=25^\circ C$	Continuous Drain Current <sup>1,6</sup>	60	A
$I_D@T_C=70^\circ C$	Continuous Drain Current <sup>1,6</sup>	60	A
$I_D@T_A=25^\circ C$	Continuous Drain Current <sup>1,6</sup>	23.8	A
$I_D@T_A=70^\circ C$	Continuous Drain Current <sup>1,6</sup>	19	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	170	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	57.8	mJ
$I_{AS}$	Avalanche Current	34	A
$P_D@T_C=25^\circ C$	Total Power Dissipation <sup>1,4</sup>	104	W
$P_D@T_A=25^\circ C$	Total Power Dissipation <sup>1,4</sup>	6.25	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient( $t \leq 10s$ ) <sup>1</sup>	---	20	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	1.2	$^\circ C/W$

**Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	80	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=10V, I_D=20A$	---	4.3	6.5	m $\Omega$
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=4.5V, I_D=20A$	---	6.3	8.5	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	---	2.3	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=64V, V_{GS}=0V, T_J=25^\circ C$	---	---	1	$\mu A$
		$V_{DS}=64V, V_{GS}=0V, T_J=55^\circ C$	---	---	5	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=5V, I_D=20A$	---	75	---	S
$R_g$	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	0.5	---	$\Omega$
$Q_g$	Total Gate Charge (10V)	$V_{DS}=40V, V_{GS}=10V, I_D=20A$	---	40	---	nC
$Q_{gs}$	Gate-Source Charge		---	7.2	---	
$Q_{gd}$	Gate-Drain Charge		---	6.5	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=40V, V_{GS}=10V, R_G=3\Omega, I_D=20A$	---	8.3	---	ns
$T_r$	Rise Time		---	4.2	---	
$T_{d(off)}$	Turn-Off Delay Time		---	36	---	
$T_f$	Fall Time		---	6.9	---	
$C_{iss}$	Input Capacitance	$V_{DS}=40V, V_{GS}=0V, f=1MHz$	---	2860	---	$\mu F$
$C_{oss}$	Output Capacitance		---	410	---	
$C_{rss}$	Reverse Transfer Capacitance		---	38	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0V$ , Force Current	---	---	60	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V, I_S=A, T_J=25^\circ C$	---	0.77	1.0	V
$t_{rr}$	Reverse Recovery Time	$I_F=20A, di/dt=100A/\mu s, T_J=25^\circ C$	---	27	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	89	---	nC

Note :

- The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
- The EAS data shows Max. rating. The test condition is  $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=34A$
- The power dissipation is limited by 150 $^\circ C$  junction temperature
- The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.
- The maximum current rating is package limited.

Typical Characteristics

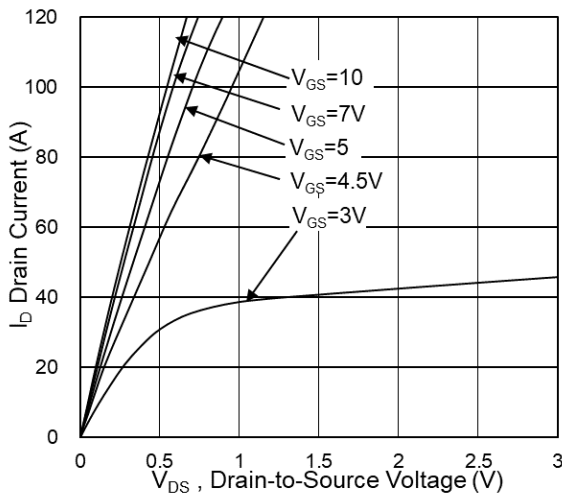


Fig.1 Typical Output Characteristics

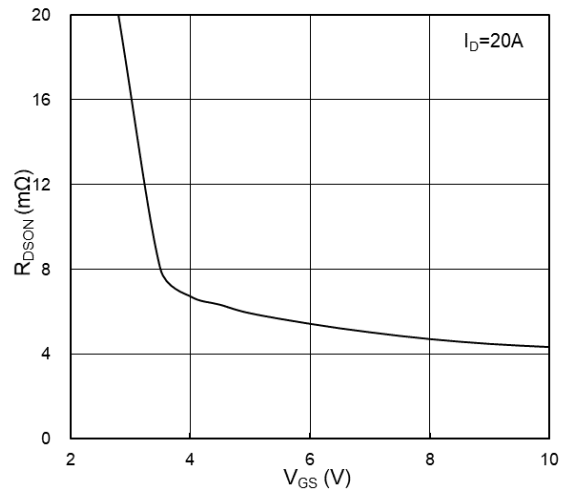


Fig.2 On-Resistance vs G-S Voltage

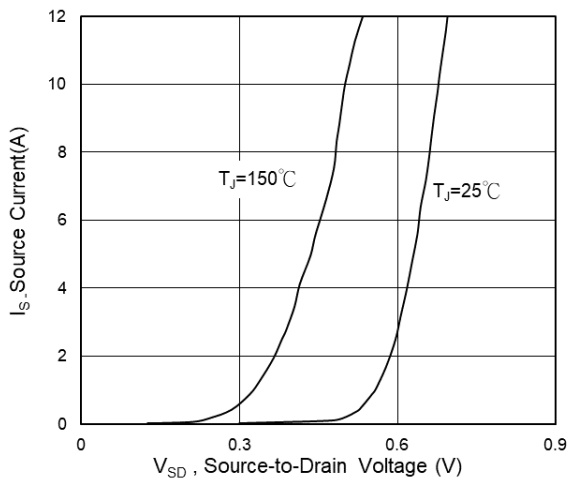


Fig.3 Source Drain Forward Characteristics

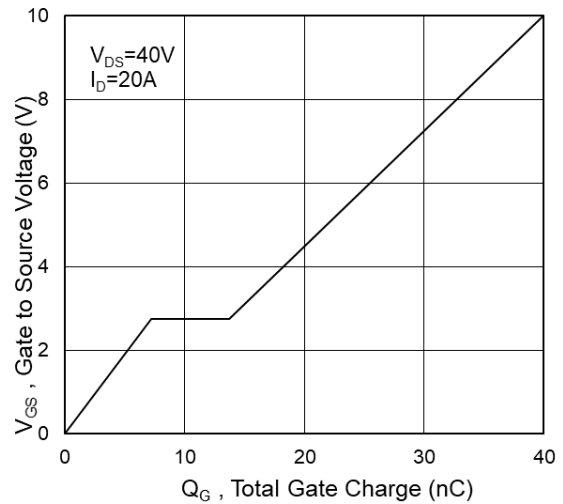


Fig.4 Gate-Charge Characteristics

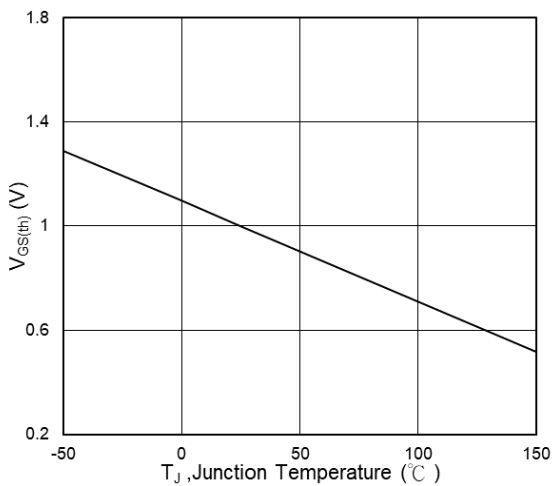


Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$

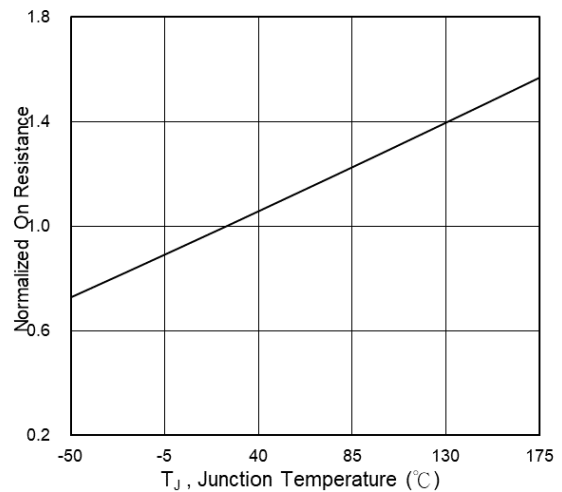


Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$

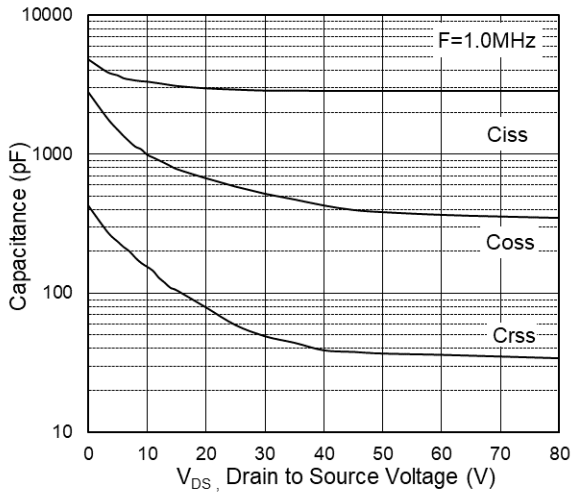


Fig.7 Capacitance

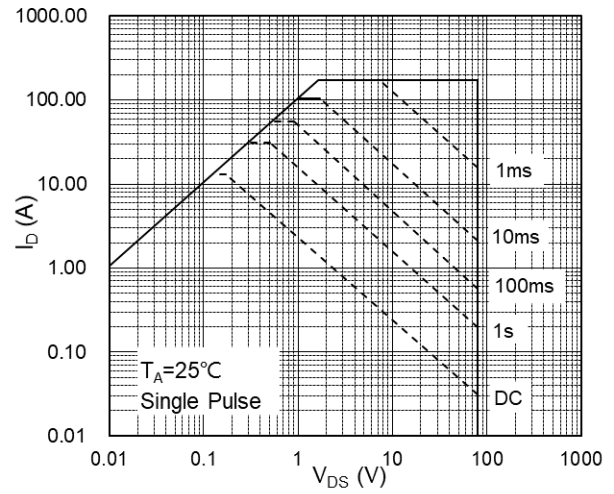


Fig.8 Safe Operating Area

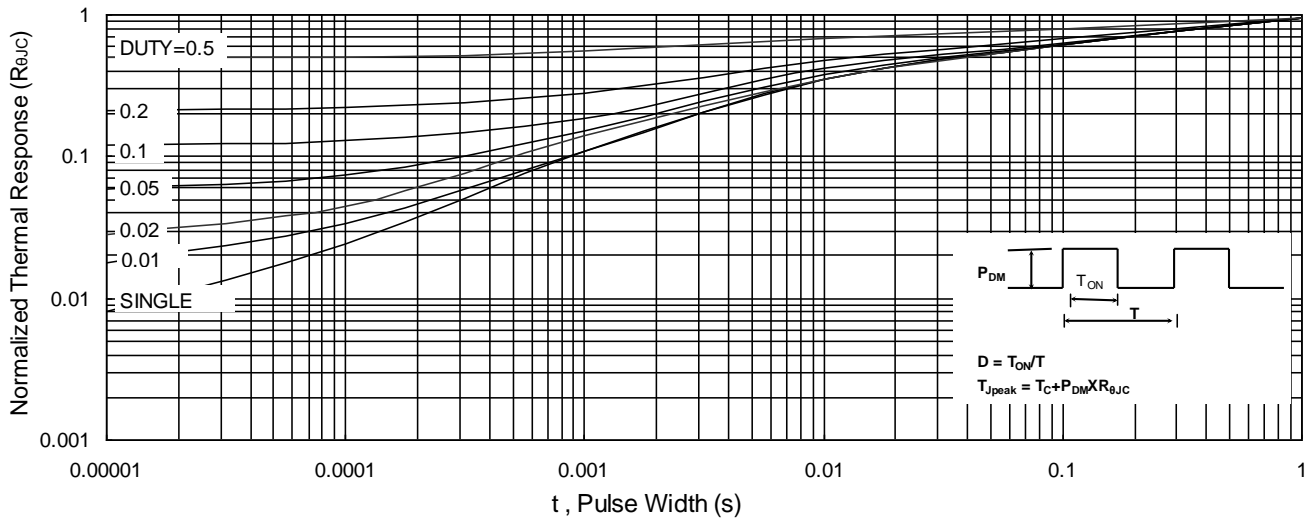


Fig.9 Normalized Maximum Transient Thermal Impedance

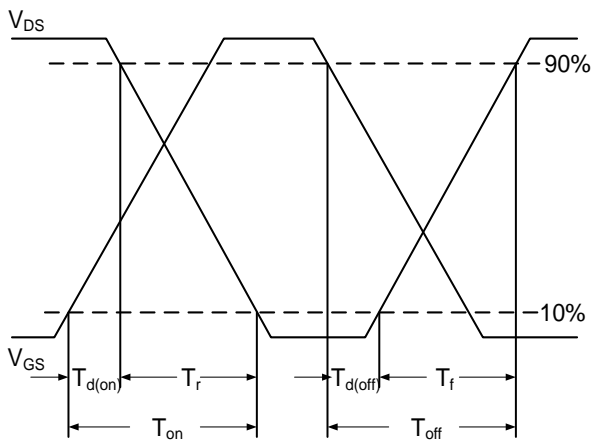


Fig.10 Switching Time Waveform

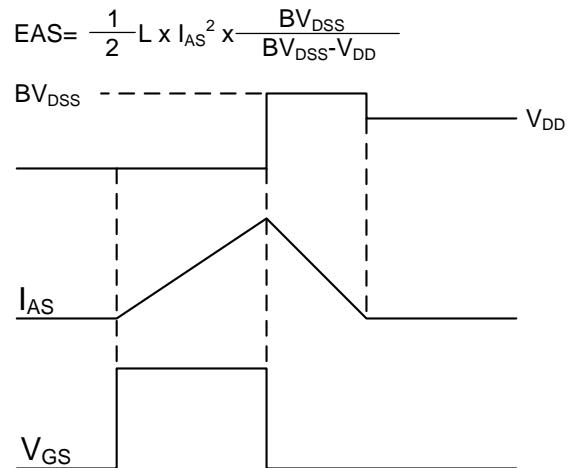


Fig.11 Unclamped Inductive Switching Waveform